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**CONFIRMATION NO. 7174** 

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APPLICANTS									<del></del>	
Farhad Pezeshki, Toronto, CANADA; Robert J. Lambert, Cambridge, CANADA;										
** CONTINUING DATA **********************************										
THIS APPLICATION IS A CIP OF PCT/CA00/00021 01/11/2000 AND A CIP OF PCT/CA00/00030 01/14/2000  J L										
** FOREIGN APPLICATIONS ************************************										
CANADA 2.258,338 01/11/1999										
CANADA 2,259,089 01/15/1999 J C										
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Acknowledged	Exan	niner's Signature Ind	tials							
ADDRESS										
Finnegan, Henderson, Farabow,										
Garrett & Dunner, L.L.P.										
1300 I Street, N.W.										
Washington ,DC 20005-3315										
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Method and apparatus for minimizing differential power attacks on processors										
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